



ELECTRONICS

Product Information

ISSUED DATE : Apr. 7, 2010

SAMSUNG TFT-LCD**MODEL : LTA550HQ03**

The Information Described in this Specification is Preliminary and can be changed without prior notice

NOTE :

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LCD Business

Samsung Electronics Co . , LTD.

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	1 / 27
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Contents

SAMSUNG SECRET

General Description	(3)
General Information	(3)
1. Absolute Maximum Ratings	(4)
1.1 Back Light Unit Absolute Maximum Ratings	
1.2 LED Unit Absolute Maximum Ratings	
2. Optical Characteristics	(6)
3. Electrical Characteristics	(9)
3.1 TFT LCD Module	
3.2 Back Light Unit	
4. Block Diagram	(11)
5. Input Terminal Pin Assignment	(12)
5.1 Input Signal & Power	
5.2 LED bar connection	
5.3 LVDS Interface	
5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color	
6. Interface Timing	(18)
6.1 Timing Parameters (DE only mode)	
6.2 Spread Spectrum	
6.3 LVDS Input Data Position	
6.4 Timing diagrams of interface signal	
6.5 Power ON/OFF Sequence	
7. Outline Dimension	(21)
8. Packing	(23)
9. Marking & Others	(24)
10. General Precaution	(25)
10.1 Handling	
10.2 Storage	
10.3 Operation	
10.4 Operation Condition Guide	
10.5 Others	

General Description

Description

LTA550HQ03 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit.

The resolution of a 55.0" is 1920 x 1080 and this model can display up to 1.07 Billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide an excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast & aperture ratio
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle ($\pm 178^\circ$)
- High speed response (240Hz)
- FHD resolution (16:9)
- Low Power consumption
- White LED Backlight
- DE Mode
- LVDS (Low Voltage Differential Signaling) Interface

General Information

Items	Specification	Unit	Note
Module Size	1267.6(H _{TYP}) x 744.4(V _{TYP})	mm	$\pm 1.0\text{mm}$
	22.9(D _{MAX})		
Weight	14.9 (Max.)	Kg	
Pixel Pitch	0.630(H) x 0.630(W)	mm	
Active Display Area	1209.6(H) X 680.4(V)	mm	
Surface Treatment	Haze 7 %, Hard-coating (3H)	-	
Display Colors	1.07 billion (Dithered 10bit)	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	450 (Typ.)	cd/m ²	

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	GND-0.3	16	V	(1)
Storage temperature		T_{STG}	-20	60	°C	(2)
Operating temperature		T_{OPR}	0	50	°C	(2)
Glass surface temperature (Operation)	Center	T_{OPR}	0	60	°C	(2)
Shock (non - operating)		S_{nop}	-	30	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

1.2 LED Unit Absolute Maximum Ratings

Item	Symbol	Max.	Unit	Note
Operating Temperature Range	T_{op}	-20~+70	°C	-
Storage Temperature Range	T_{STG}	-30~+70	°C	-
Junction Temperature	T_j	125	°C	-
Forward Current	I_f	TBD	A	Continuous operation @ 1Bar
	I_{fp}	TBD	A	Duty 33% operation @ 1Bar
Forward Voltage	V_f	TBD	V	Continuous operation @ 1Bar
	V_{fp}	TBD	V	Impulsive operation @ 1Bar
Thermal Resistance, Junction to PCB	$R_{th,JS}$	<47.9K/W	K/W	-

- Note (1) $T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$
(2) Temperature and relative humidity range are shown in the figure below.
a. 90 % RH Max. ($T_a \leq 39 \text{ }^{\circ}\text{C}$)
b. Relative Humidity is 90% or less. ($T_a > 39 \text{ }^{\circ}\text{C}$)
c. No condensation
(3) 11ms, sine wave, one time for $\pm X, \pm Y, \pm Z$ axis
(4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

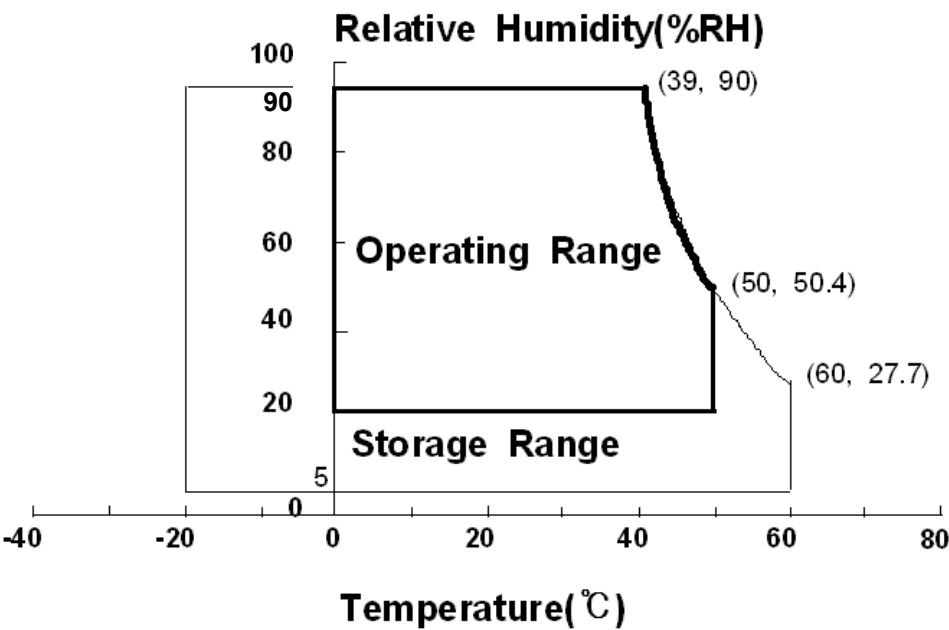


Fig. Temperature and Relative humidity range

2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON RD-80S, TOPCON SR-3 ,ELDIM EZ-Contrast

(Ta = 25 ± 2 °C, VDD=12.0V, fv=60Hz, f_{DCLK}=148.5MHz, LED current=110mA)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal $\theta_{\mathbf{L}},\mathbf{R}=0$ $\theta_{\mathbf{U}},\mathbf{D}=0$ Viewing Angle	TBD	5000	-		(1) SR-3
Response Time	G-to-G	Tg		-	6	-	msec	(3) RD-80S
Luminance of White (Center of screen)		Y _L		TBD	450	-	cd/m ²	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	TBD	TYP. +0.03		(5),(6) SR-3
		Ry			TBD			
	Green	Gx			TBD			
		Gy			TBD			
	Blue	Bx			TBD			
		By			TBD			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	(72)	-	%	(5) SR-3
Color Temperature		-		-	10,000	-	K	
Viewing Angle	Hor.	θ _L	C/R≥10	75	89	-	Degree	(6) EZ-Contrast
		θ _R		75	89	-		
	Ver.	θ _U		75	89	-		
		θ _D		75	89	-		
Brightness Uniformity (9 Points)		B _{uni}		-	-	25	%	(2) SR-3

- Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

. LED current = 110mA

. Environment condition : Ta = 25 ± 2 °C

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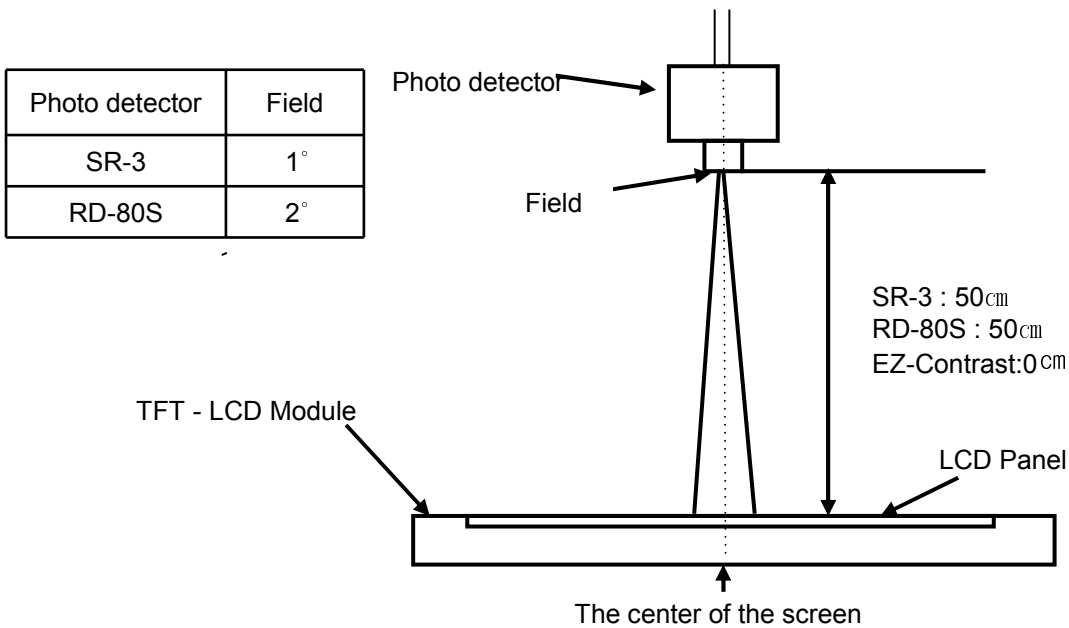
LTA550HQ03

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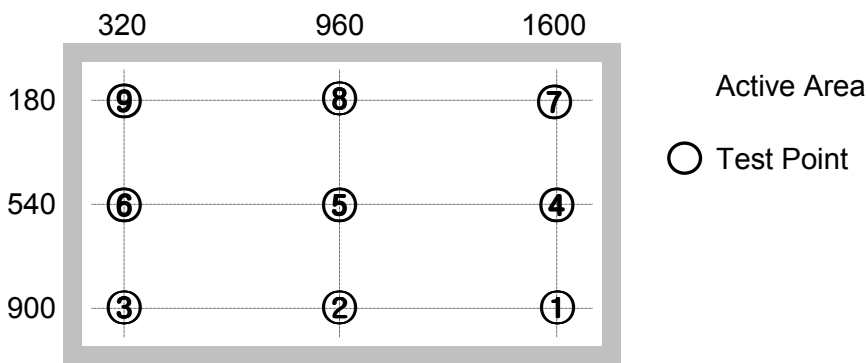
06-000-G-100407

Page

6 / 27



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C / R = \frac{G \max}{G \min}$$

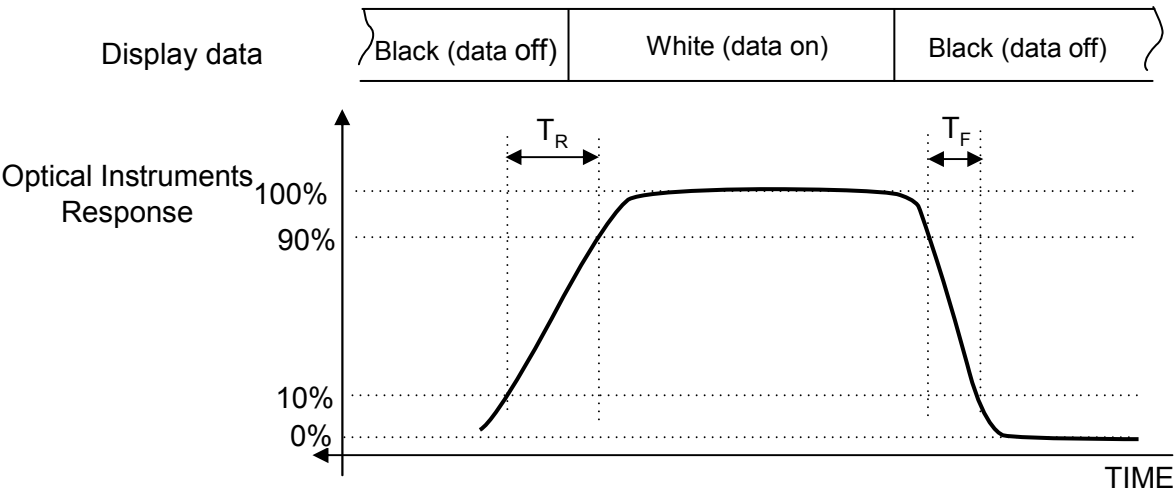
Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100 * \frac{(B \text{ max} - B \text{ min})}{B \text{ max}}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf

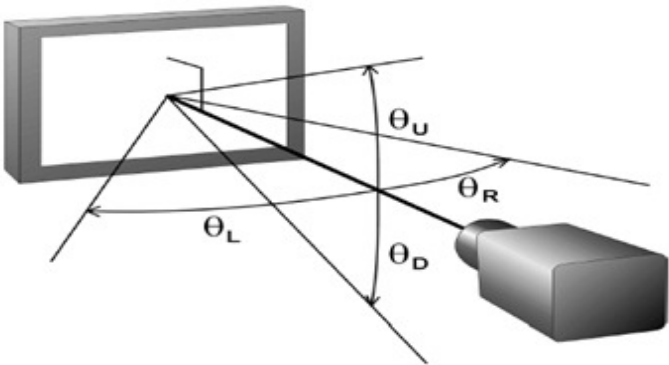


※ G- to- G : Average response time between Gray to Gray (Scale)

Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle
: Viewing angle range ($C/R \geq 10$)



3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

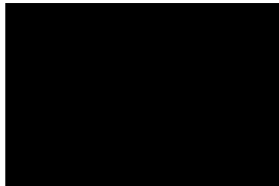
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V _{DD}	10.8	12	13.2	V	(1)
Current of Power Supply	(a) Black	I _{DD}	-	TBD	TBD	mA	(2),(3)
	(b) White		-	TBD	TBD	mA	
	(d) H-stripe		-	TBD	TBD	mA	
Rush Current		I _{RUSH}	-	-	7	A	(4)
			Video	PAL	NTSC	Unit	
Vsync Frequency		f _V	48	50	60	Hz	
Hsync Frequency		f _H	64.8	67.5	67.5	kHz	
Main Frequency		Fdclk	142.56	148.5	148.5	MHz	

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

(2) $f_v=60\text{Hz}$, $f_{DCLK}=148.5\text{MHz}$, $V_{DD}=12.0\text{V}$, DC Current.

(3) Power dissipation check pattern (LCD Module only)

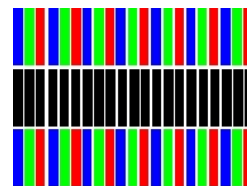
a) Black Pattern



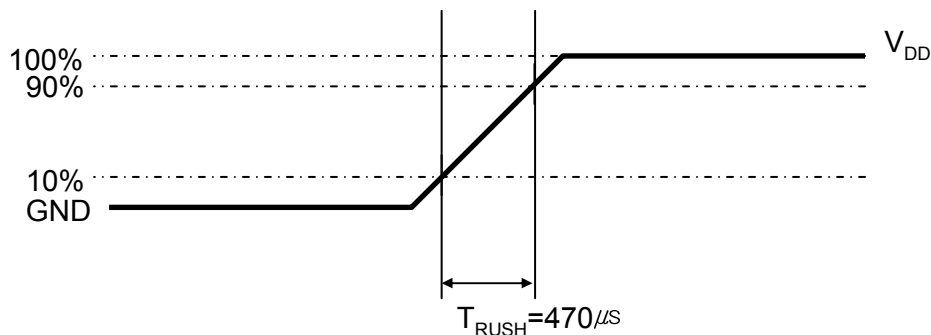
b) White Pattern



c) H-stripe



(4) Measurement Conditions

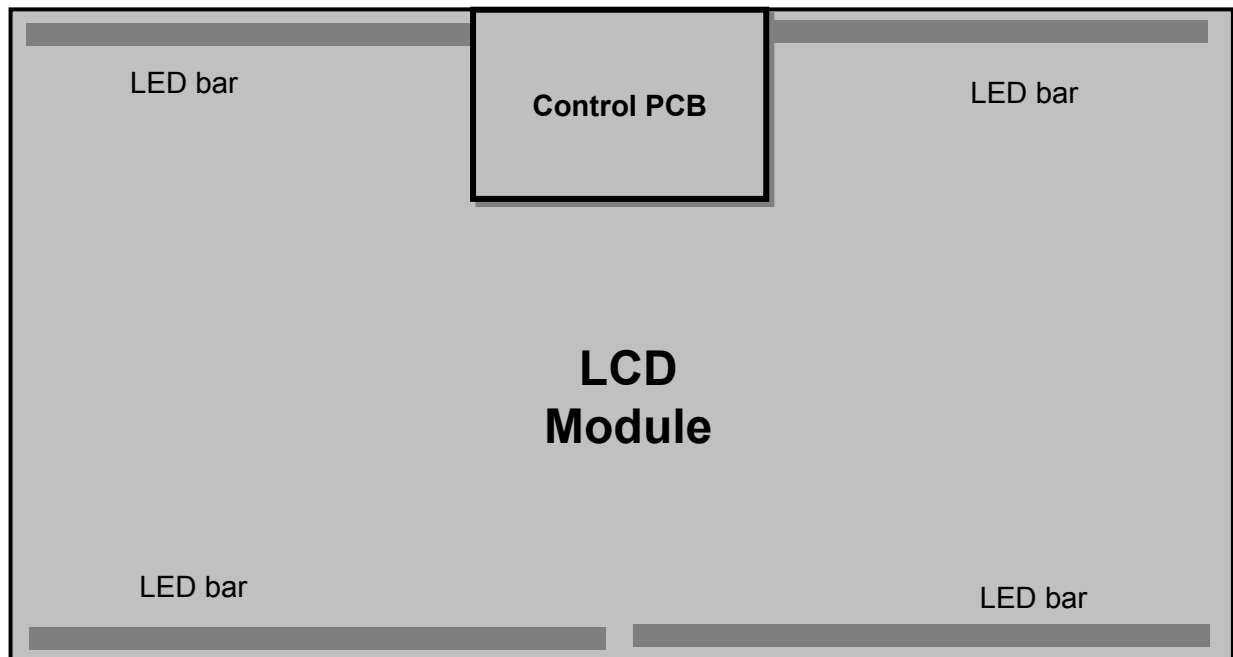


Rush Current I_{RUSH} can be measured when T_{RUSH} is $470 \mu s$.

3.2 Back Light Unit

 $T_a = 25 \pm 2^{\circ}\text{C}$

- Back light unit is composed of 4 -LED -bars .



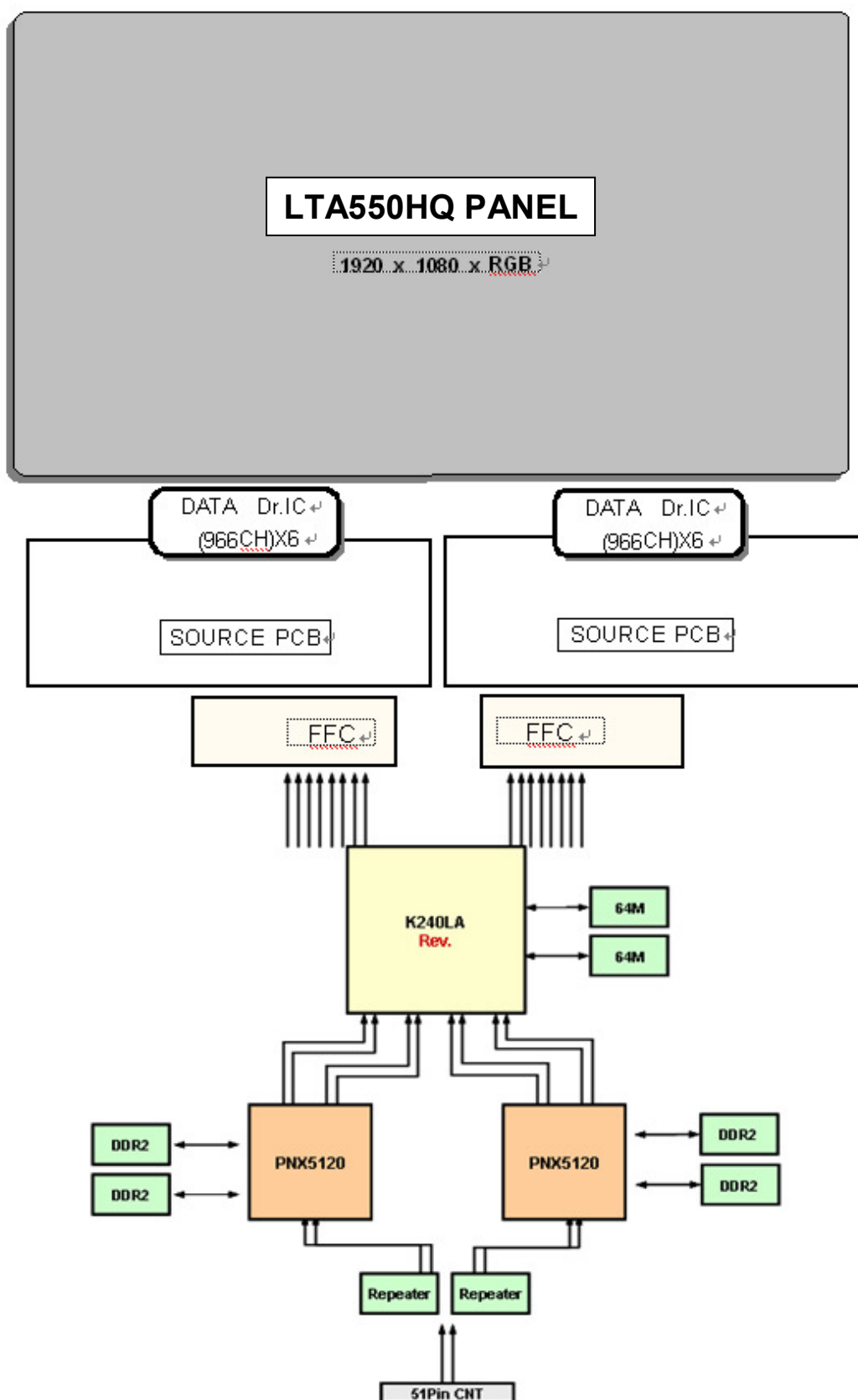
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	TBD	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : $T_a = 25 \pm 2^{\circ}\text{C}$]

4. Block Diagram

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MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	11 / 27
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5. Input Terminal Pin Assignment

5.1. Input Signal & Power

Connector : FI-RE51S-HF (JAE)

Pin	Symbol	Description	Pin	Symbol	Description
1	12V	DC power supply	26	RE[0]P	Even LVDS Signal +
2	12V	DC power supply	27	RE[1]N	Even LVDS Signal -
3	12V	DC power supply	28	RE[1]P	Even LVDS Signal +
4	12V	DC power supply	29	RE[2]N	Even LVDS Signal -
5	12V	DC power supply	30	RE[2]P	Even LVDS Signal +
6	NC	NOTE1	31	GND	Ground
7	GND	Ground	32	ROCLK-	Even LVDS Clock -
8	GND	Ground	33	ROCLK+	Even LVDS Clock +
9	GND	Ground	34	GND	Ground
10	RO[0]N	Odd LVDS Signal -	35	RE[3]N	Even LVDS Signal -
11	RO[0]P	Odd LVDS Signal +	36	RE[3]P	Even LVDS Signal +
12	RO[1]N	Odd LVDS Signal -	37	RE[4]N	Even LVDS Signal -
13	RO[1]P	Odd LVDS Signal +	38	RE[4]P	Even LVDS Signal +
14	RO[2]N	Odd LVDS Signal -	39	GND	Ground
15	RO[2]P	Odd LVDS Signal +	40	NC	NOTE1
16	GND	Ground	41	NC	
17	ROCLK-	Odd LVDS Clock -	42	NC	
18	ROCLK+	Odd LVDS Clock +	43	NC	
19	GND	Ground	44	INV_SYNC	Inverter Sync. Signal out
20	RO[3]N	Odd LVDS Signal -	45	INV_PWM	Inverter Dimming Signal out
21	RO[3]P	Odd LVDS Signal +	46	I2C_SCL_F	Set Use Only
22	RO[4]N	Odd LVDS Signal -	47	FRC_nRESET	Set Use Only
23	RO[4]P	Odd LVDS Signal +	48	I2C_SDA_F	Set Use Only
24	GND	Ground	49	SW_PVCC	Set Use Only
25	RE[0]N	Even LVDS Signal -	50	MAIN_CHECK	NOTE2
			51	NC	NOTE1

Note1) No Connection: These PINS are used only for SAMSUNG. (DO NOT CONNECT)

Note2) MAIN_CHECK ; HIGH (3.3V) → AUTO SCRIPT Enable mode

LOW (GND) → AUTO SCRIPT Disable mode

(In this case, customer can control FRC chip.)

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	12 / 27
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Note(1) Pin number starts from left side

SAMSUNG SECRET

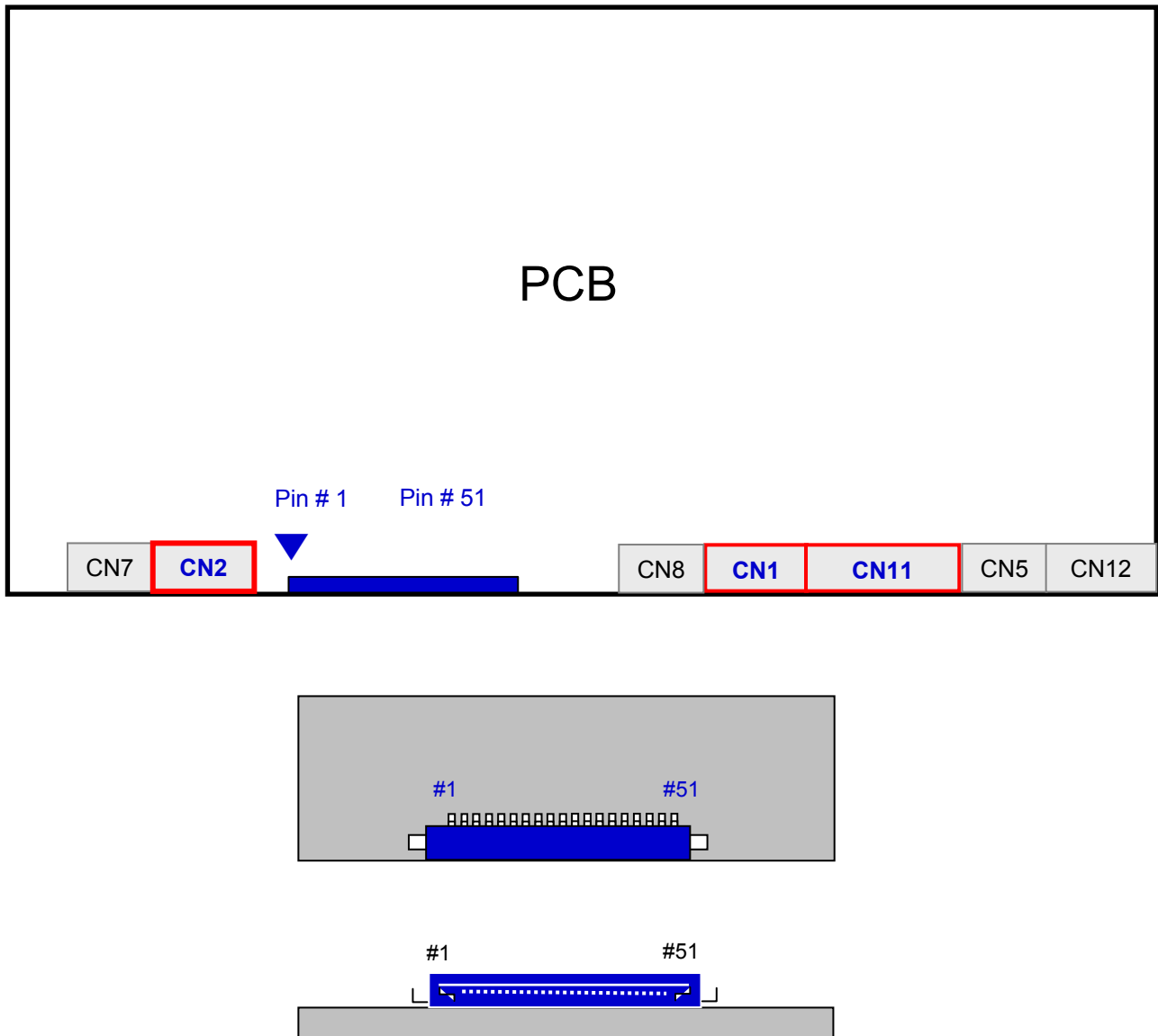


Fig. Connector diagram

- a. Power GND pins should be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pin should be separated from other signal or power.
- d. **CN1,2 and 11 are open connectors to the customer.**
Others are used only for SAMSUNG. Do not connect.

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	13 / 27
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<CN1 >**Connector : KN30-4P-1.25H**

Pin	Symbol	Description
1	T1OUT_F	CMOS 3.3V LVCMOS, UART1 transmit
2	RIN_F	CMOS 3.3V LVCMOS, UART1 receive
3	GND	Ground
4	NC	NC

<CN11 >**Connector : 12505WR**

Pin	Symbol	Description
1	F_TRSTN	CMOS 3V3, 5V tolerant inputs, JTAG test reset for Master
2	F_TDI	CMOS 3V3, 5V tolerant inputs, JTAG test serial data input for Master
3	F_TDO	CMOS 3V3, 5V tolerant inputs, JTAG test serial data input for Master
4	F_TMS	CMOS 3V3, 5V tolerant inputs, JTAG test mode select for Master
5	F_TCK	CMOS 3V3, 5V tolerant inputs, JTAG test clock for Master
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	GND	Ground
10	GND	Ground

<CN2 >**Connector : 20037WR-06A00**

Pin	Symbol	Description
1	Vin_1B	Set Power in
2	Vin_1B	Set Power in
3	Vin_1B	Set Power in
4	GND	GND
5	GND	GND
6	GND	GND

5.2 LED Bar connector

Connector : 20037WR-15 (YEONHO)

Pin	Description	LED Bar Connection
1	+	The Top-Left LED Bar
2	NC	
3	-	
4	NC	
5	+	The Bottom-Left LED Bar
6	NC	
7	-	
8	NC	
9	+	The Bottom-Right LED Bar
10	NC	
11	-	
12	NC	
13	+	The Top-Right LED Bar
14	NC	
15	-	

5.3 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA Only)

	LVDS pin	JEIDA DATA
TxOUT/RxIN0	TxIN/RxOUT0	R4
	TxIN/RxOUT1	R5
	TxIN/RxOUT2	R6
	TxIN/RxOUT3	R7
	TxIN/RxOUT4	R8
	TxIN/RxOUT6	R9
	TxIN/RxOUT7	G4
TxOUT/RxIN1	TxIN/RxOUT8	G5
	TxIN/RxOUT9	G6
	TxIN/RxOUT12	G7
	TxIN/RxOUT13	G8
	TxIN/RxOUT14	G9
	TxIN/RxOUT15	B4
	TxIN/RxOUT18	B5
TxOUT/RxIN2	TxIN/RxOUT19	B6
	TxIN/RxOUT20	B7
	TxIN/RxOUT21	B8
	TxIN/RxOUT22	B9
	TxIN/RxOUT24	HSYNC
	TxIN/RxOUT25	VSYNC
	TxIN/RxOUT26	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2
	TxIN/RxOUT5	R3
	TxIN/RxOUT10	G2
	TxIN/RxOUT11	G3
	TxIN/RxOUT16	B2
	TxIN/RxOUT17	B3
	TxIN/RxOUT23	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0
	TxIN/RxOUT29	R1
	TxIN/RxOUT30	G0
	TxIN/RxOUT31	G1
	TxIN/RxOUT32	B0
	TxIN/RxOUT33	B1
	TxIN/RxOUT34	RESERVED

5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																												GRAY SCALE LEVEL		
		RED										GREEN										BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7		B8	B9
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-	
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-	
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK ↑ ↓ LIGHT	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023	
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK ↑ ↓ LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	G1023	
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑ ↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B1	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1023	

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

6. Interface Timing

6.1 Timing Parameters (FRC mode)

SIGNAL	ITEM	SYMBOL	Video	PAL	NTSC	UNIT	NOTE
Clock	Frequency	1/TC	142.56	148.5	148.5	MHz	-
Hsync		Fh	64.8	67.5	67.5	KHz	-
Vsync		Fv	48	50	60	Hz	-
Vertical Active Display Term	Display Period	TVD	1080	1080	1080	lines	-
	Vertical Total	TV	1350	1350	1125	lines	-
Horizontal Active Display Term	Display Period	THD	1920	1920	1920	clocks	-
	Horizontal Total	TH	2200 (1100*2)	2200 (1100*2)	2200 (1100*2)	clocks	-

Note) This product is DE mode. The input of Hsync & Vsync signal is essentially needed.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

(2) Internal $V_{DD} = 3.3V$

6.2 Spread Spectrum

- Modulation rate (max) : $\pm 2.8 \%$
- Modulation Frequency : under 62.5KHz

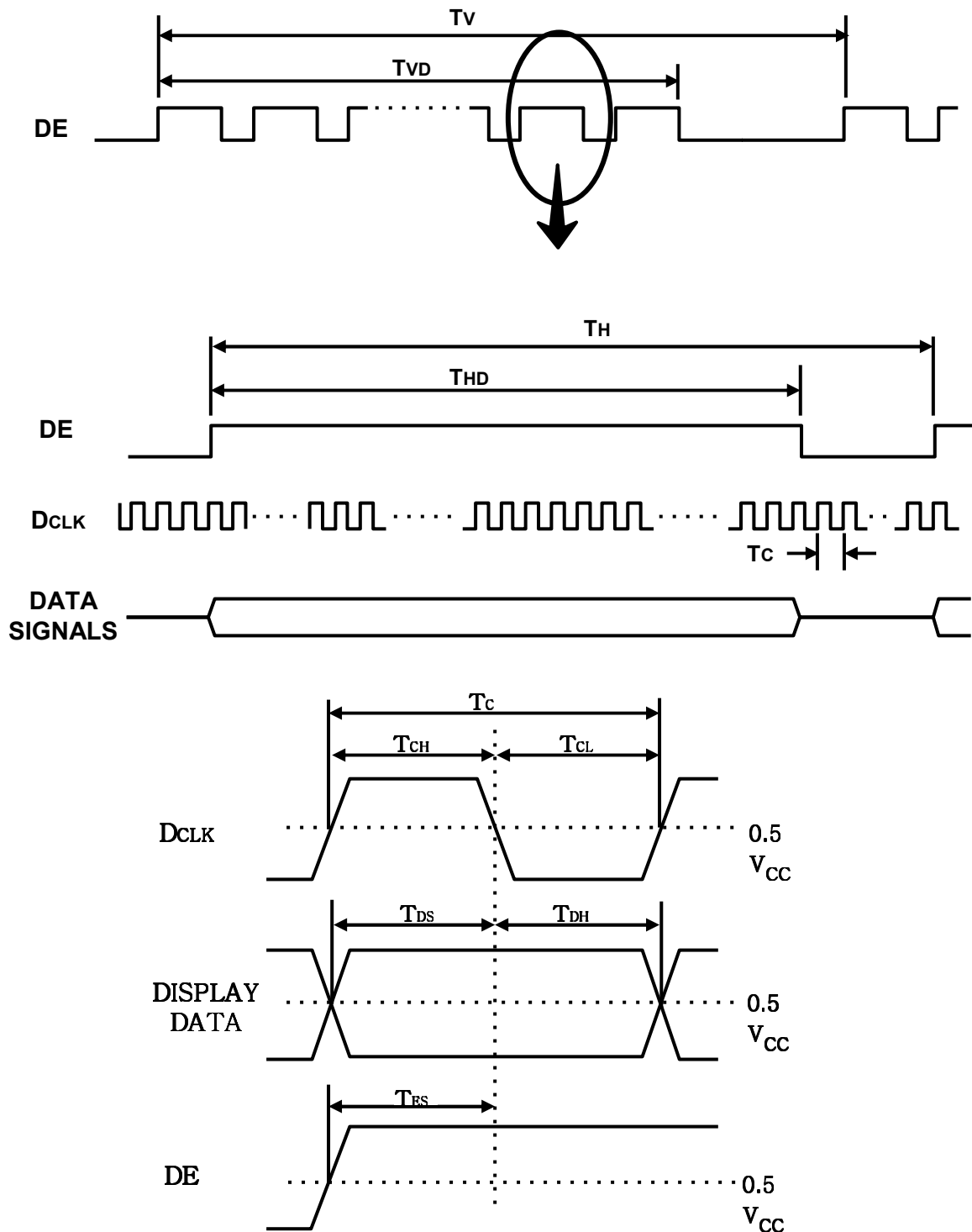
6.3 LVDS Input Data Position

ITEM		SYMBOL	Min.	Typ.	Max.	UNIT	NOTE
Input Data Position	$F_{IN}=85MHz$	t_{RSM}	-	-	TBD	ps	-
	$F_{IN}=78MHz$		-	-	TBD	ps	
	$F_{IN}=75MHz$		-	-	TBD	ps	
Input Data Position	$F_{IN}=85MHz$	t_{RSLM}	TBD	-	-	ps	
	$F_{IN}=78MHz$		TBD	-	-	ps	
	$F_{IN}=75MHz$		TBD	-	-	ps	
Input common mode voltage		V_{CM}	0.3	-	1.8	V	-
Differential Input Voltage		$ V_{ID} $	150	350	600	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

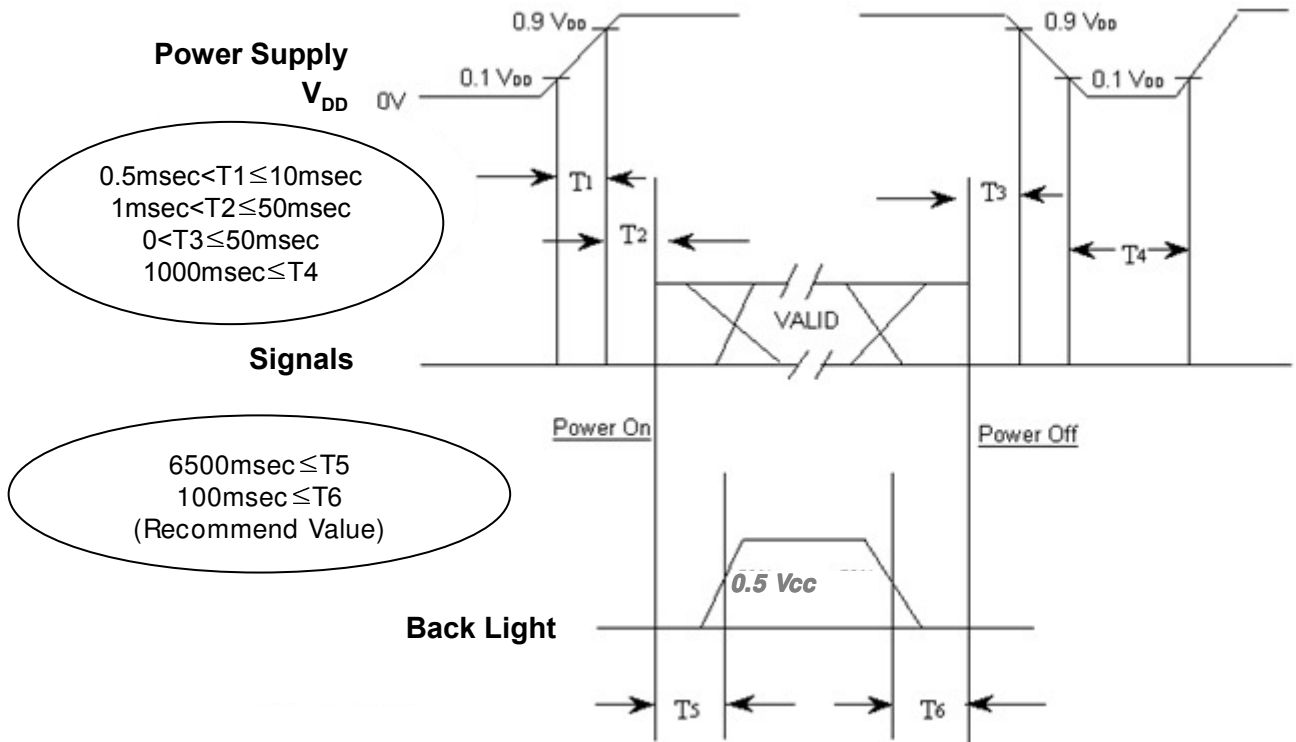
MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	18 / 27
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6.4 Timing diagrams of interface signal (FRC mode)



6.5 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



- T_1 : V_{DD} rising time from 10% to 90%
- T_2 : The time from V_{DD} to valid data at power ON.
- T_3 : The time from valid data off to V_{DD} off at power Off.
- T_4 : V_{DD} off time for Windows restart
- T_5 : The time from valid data to B/L enable at power ON.
- T_6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD} .
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T_4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

7. Outline Dimension

T

B

D

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	21 / 27
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7. Outline Dimension

T

B

D

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	22 / 27
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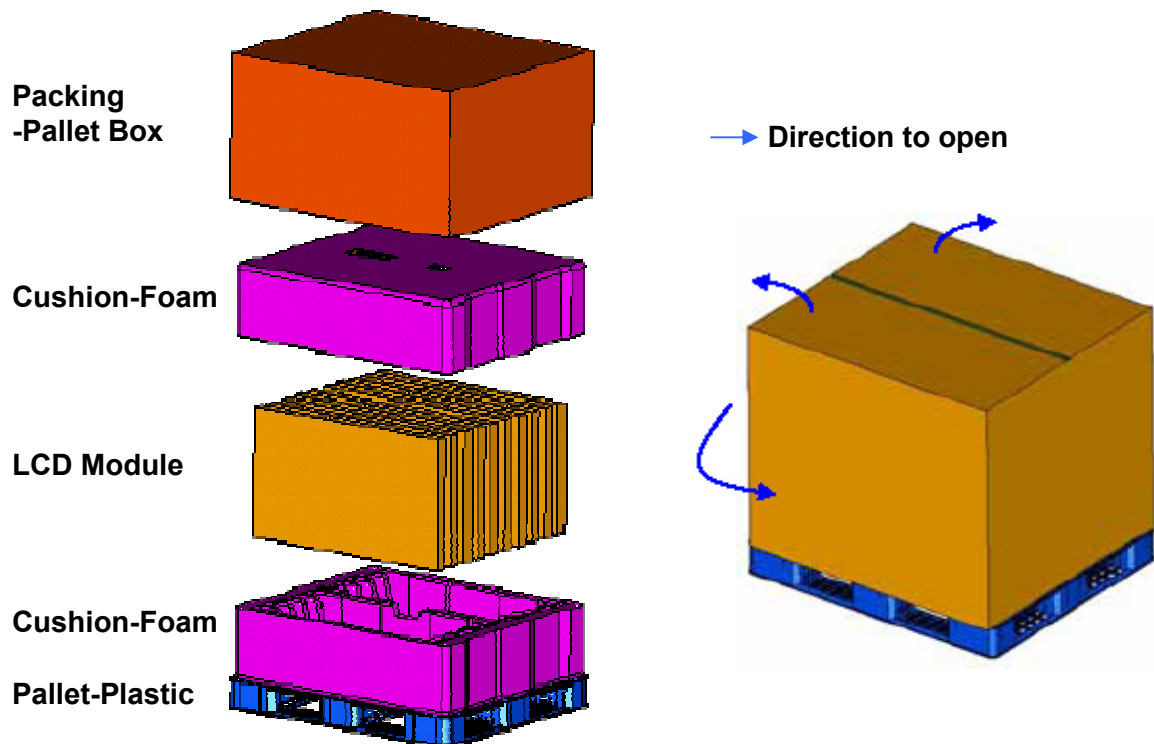
8. PACKING

8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



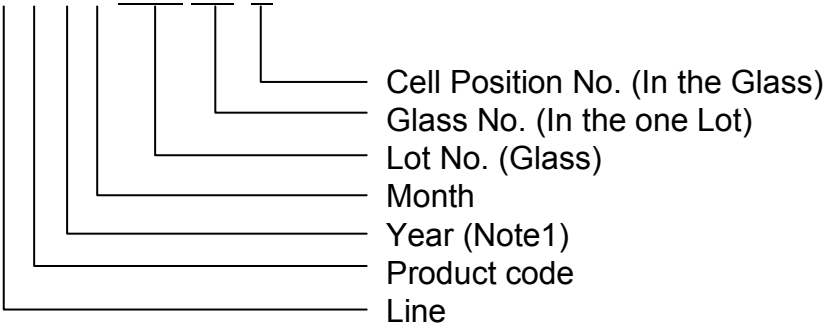
8.2 Packing Specification

Item	Specification	Remark
LCD Packing	13ea / (Packing-Pallet Box)	1. 182 kg / LCD (13ea) 2. 15.3 Kg / Cushion-pallet (2ea) 3. 10.5 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : SW4
Pallet	1Box / Pallet	1. Pallet weight = 10kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1475mm(H) x 1150mm(V) x 995mm(height)
Total Pallet Weight	217.8 kg	Pallet(10kg) + Module (182kg) + Cushion (up + bottom =15.3kg) + Pallet-BOX(10.5kg)

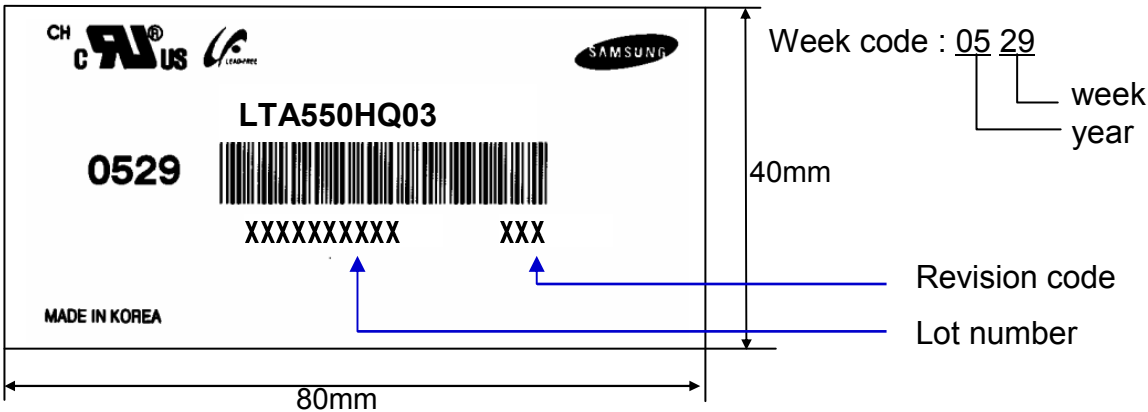
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

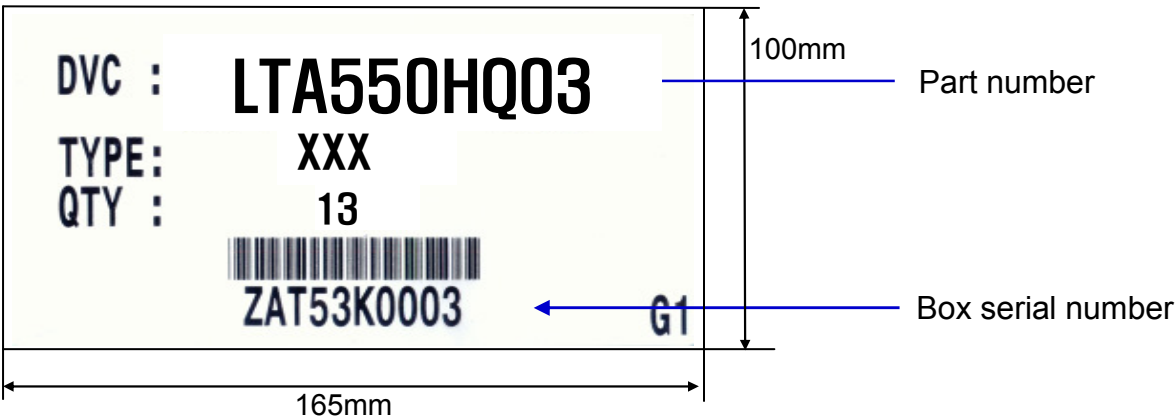
- (1) Part number : LTA550HQ03
- (2) Revision: Three letters
- (3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



10. General Precautions

10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module.
In addition to damage, this may cause improper operation or damage to the Module and LED back light.
- (d) Note that polarizers are very fragile and could be damage easily.
Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.
Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not disassemble shield case of inverter & LVDS board.
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	25 / 27
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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 °C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

10.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
 - Temperature : $20 \pm 15^{\circ}\text{C}$
 - Humidity : $55 \pm 20\%$
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

MODEL	LTA550HQ03	Doc. No	06-000-G-100407	Page	26 / 27
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10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked " to the screen.
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.